APPLICATION NO. CONT/PRIOR CLASS 7/2 SUBCIASS ARTUNIT 2/2 EXAMINER ARTURIT 2/2 EXAMINER ARTUR	835 Jeagly S. 870	12/20/00 12/20/00 Class Subclass	ISSUE CLASSIFICATION	DEST U.S. UTI KC	ILITY Pater		OPY On ITENT DATE	PATENT NUMBE	A
Parallel dispatch wait signaling method, method for reducing contention of highly contended dispatcher lock and related		09/751659			SUBCLASS S	ART UNIT 2	EXAMINER \	FULTADO,	
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TERMINAL	DRAWINGS		CLAIMS ALLOWED			
LIDISCLAIMER	Sheets Drwg. Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.		
The term of this patent subsequent to (date)			NOTICE OF ALL	OWANCE MAILED		
has been disclaimed.	(Assistant Examiner)	(Date)		•		
The term of this patent shall not extend beyond the expiration date						
of U.S Patent. No.			ISSUE FEE			
		Ī	Amount Due	Date Paid		
	(Primary Examiner)	(Date)				
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uns patent have been discialined.	(Legal instruments Examiner)	(Date)	e = 3			
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